



Detailed Specifications

Feature		AVG Timer DC models	
Part Number		AVG-CKK-466 (Cycle, 8-pin, DPDT) AVG-T1K-466 (Time on Delay, 8-pin, DPDT) AVG-T2K-466 (Single Shot, 11-pin, DPDT) AVG-T3K-466 (Time off Delay, 11-pin, DPDT) AVG-TM4-466 (Programmable, 11-pin, DPDT)	AVG-CKK-462 (Cycle, 8-pin, DPDT) AVG-T1K-462 (Time on Delay, 8-pin, DPDT) AVG-T2K-462 (Single Shot, 11-pin, DPDT) AVG-T3K-462 (Time off Delay, 11-pin, DPDT) AVG-TM4-462 (Programmable, 11-pin, DPDT)
Source Voltage		12 VDC	24 VDC
Permitted Power Fluctuation		10.8 - 13.2 VDC	21.6 - 26.4 VDC
Power Consumption		3 VA Max.	
Memory Backup upon Power Failure		EEPROM writing up to 100,000 times; Memory duration:10years	
Ambient Temperature		-20-60°C (-4 to 140°F)	
Storage Temperature		-50-150°C (-58 to 302°F)	
Ambient Humidity		35-85% RH non-condensing	
Withstand Voltage		2KVAC for 1 min	
Vibration Resistance		Displacement amplitude of 1mm at 10-60 Hz along 3 axes	
Impact Resistance		Durability: 500 m/s ² along three axes; Operating impact: 100 m/s ² along three axes	
Noise Resistance		DC power between terminals ±1.0kV (pulse width 1µs and rise time 1ns)	
Protective Structure		IP65 (front panel only) when mounted in appropriate enclosure	
Socket (8-pin/11-pin)	Conforming wiring	12 to 22 AWG	
	Permitted Torque	0.5Nm (0.369 ft/lbs)	
Performance Specification			
Operational Format		On-delay, off-delay, single-shot, cycle and programmable (all 4 modes in one)	
Number of Digits		3 digits	
Display		Large bright red LED with 12mm character height	
Time Range		0.01s-999 hrs	
Display Read-out		Remaining/Elapsed time, Setpoint, Type (T1, T2, T3, CYC), Password Protection	
Timer Precision		0.1% or ±20 ms (whichever is greater)	
Start Input Response		Less than 15 ms/5 ms/1 ms	
External Reset		Min. signal amplitude 2 ms (typical)	
Output		Relay output: DPDT(Double Pole Double Throw) 28 VDC, 7A (resistive load). 100mA @ 5 VDC minimum	
Output Duration (flicker)		10ms/1sec/1min/1Hr variable every 3ms	
Installation		8-pin or 11-pin relay socket mounting	
Agency Approval		UL, cUL, CE (pending)	